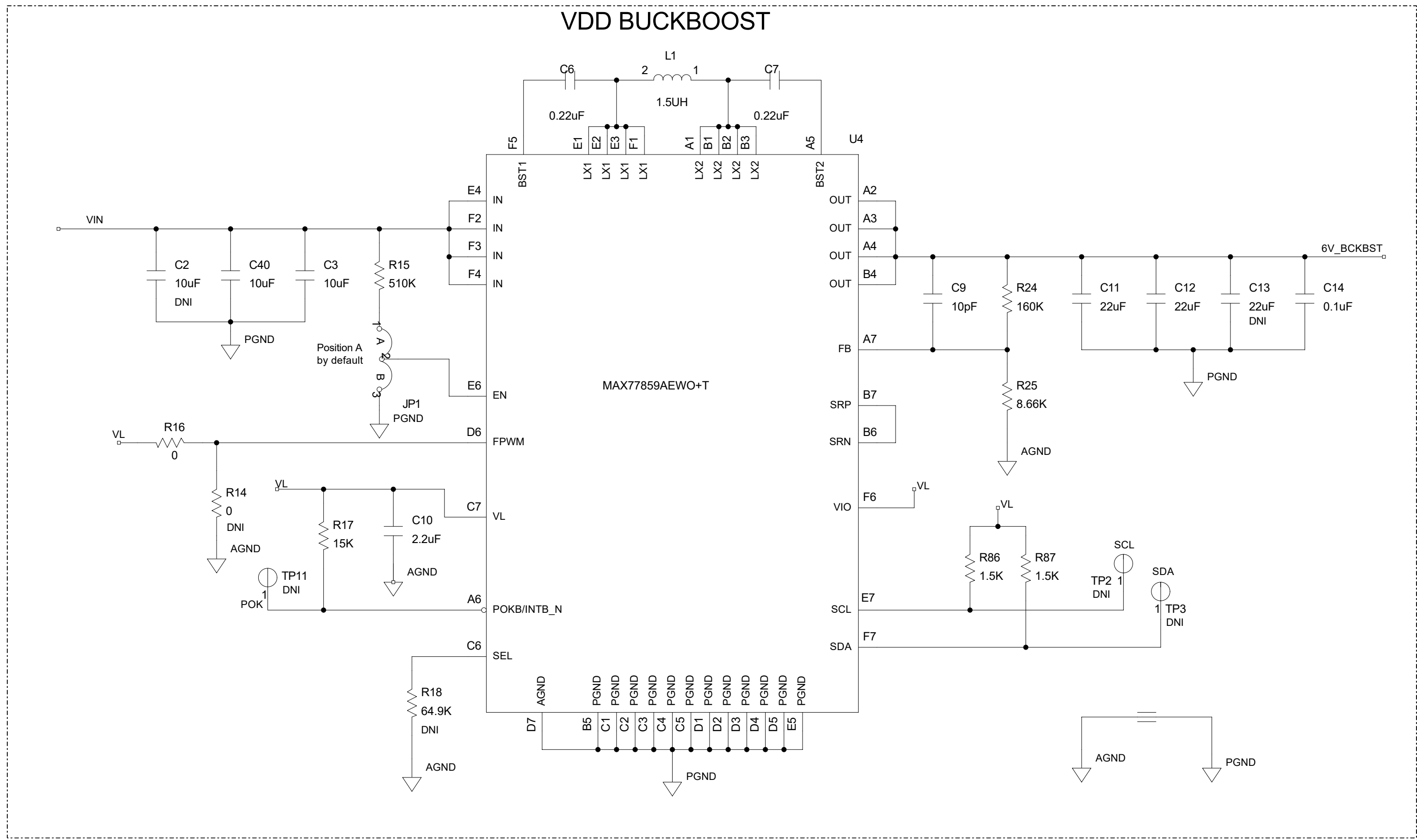
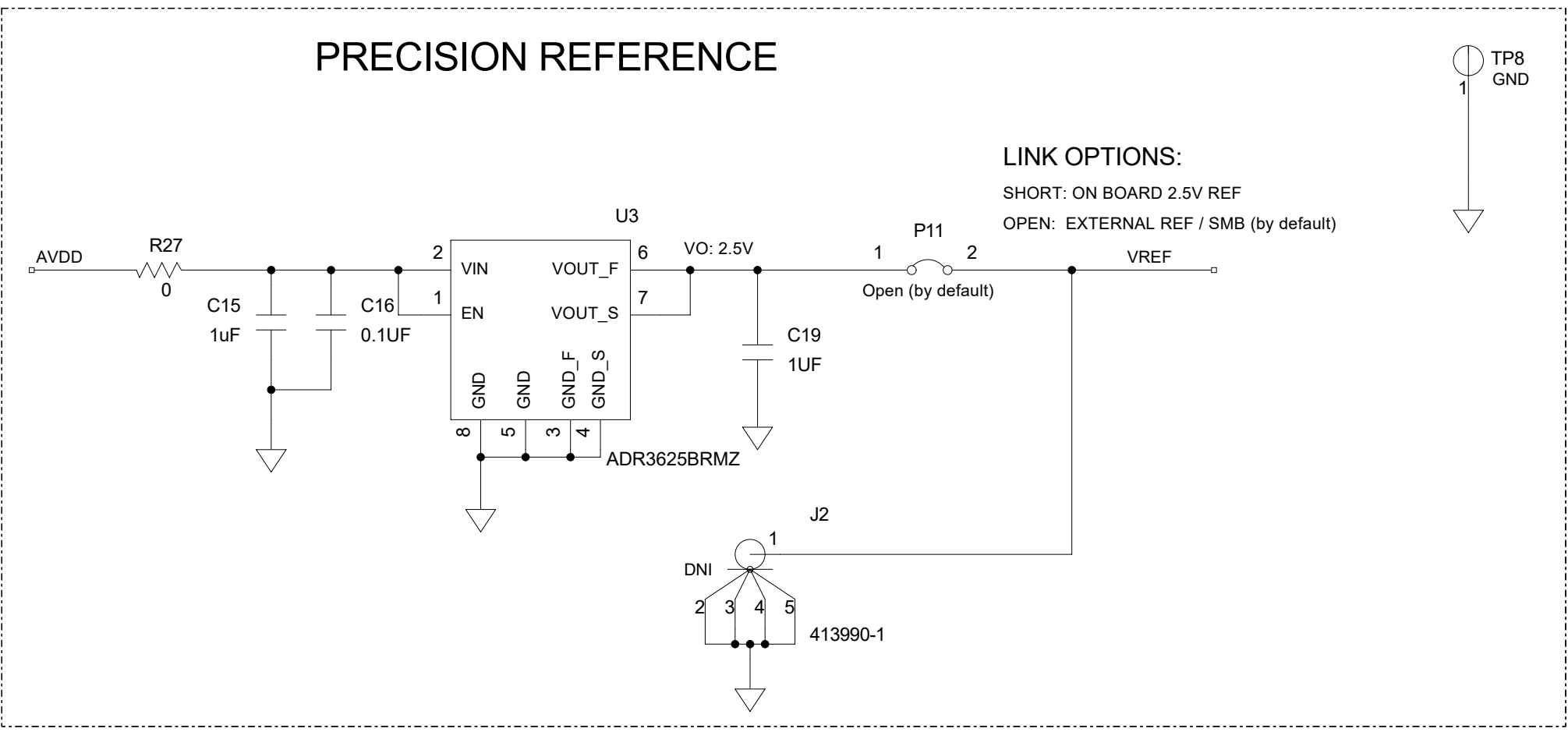
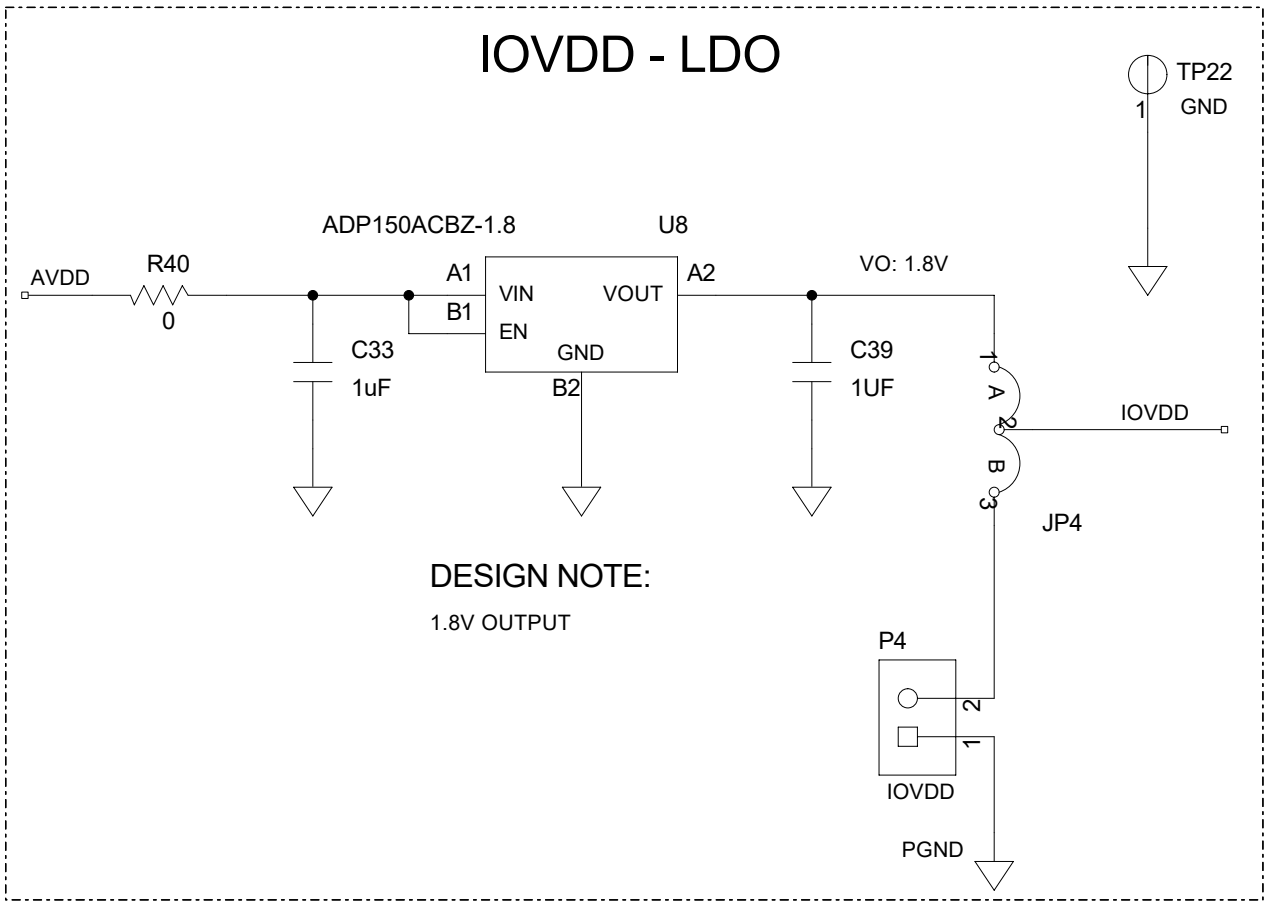
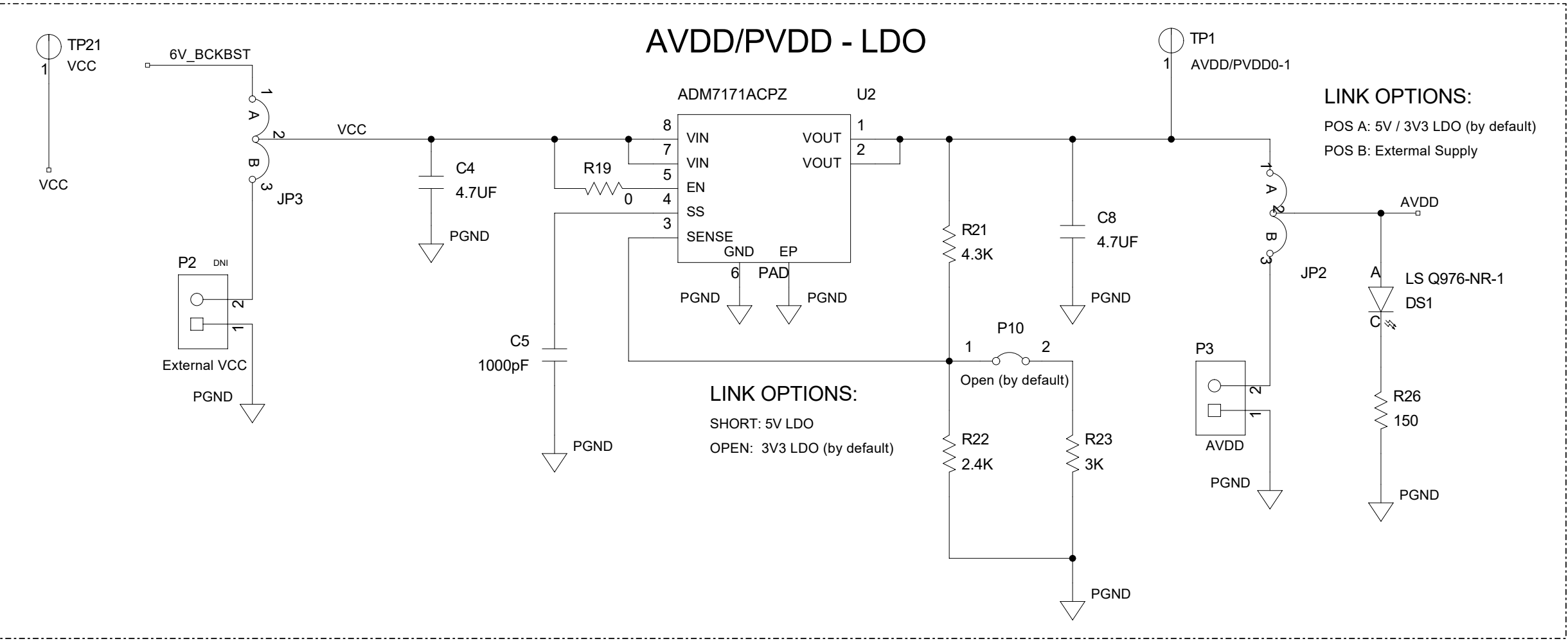
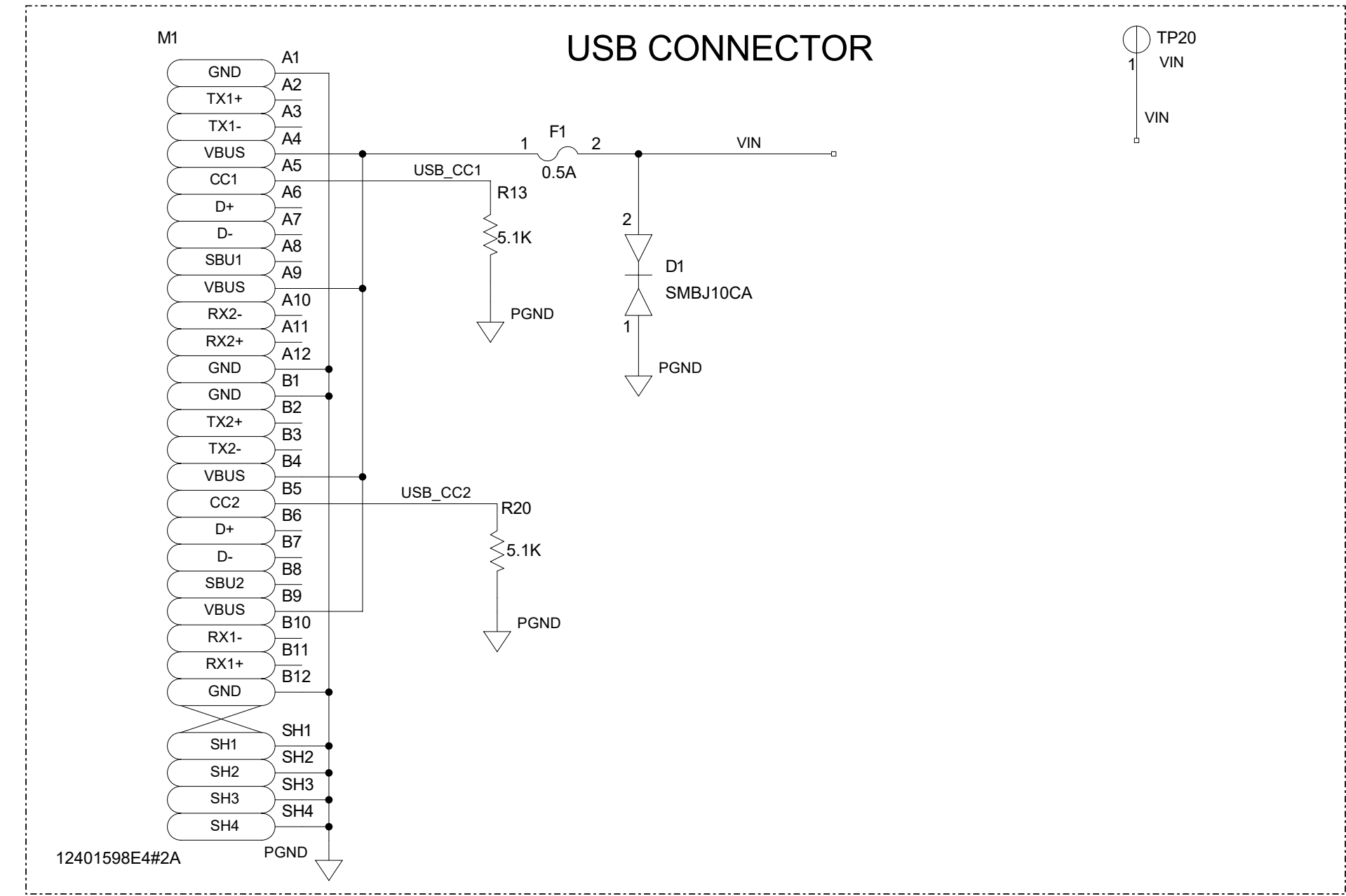


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

POWER CIRCUITRY

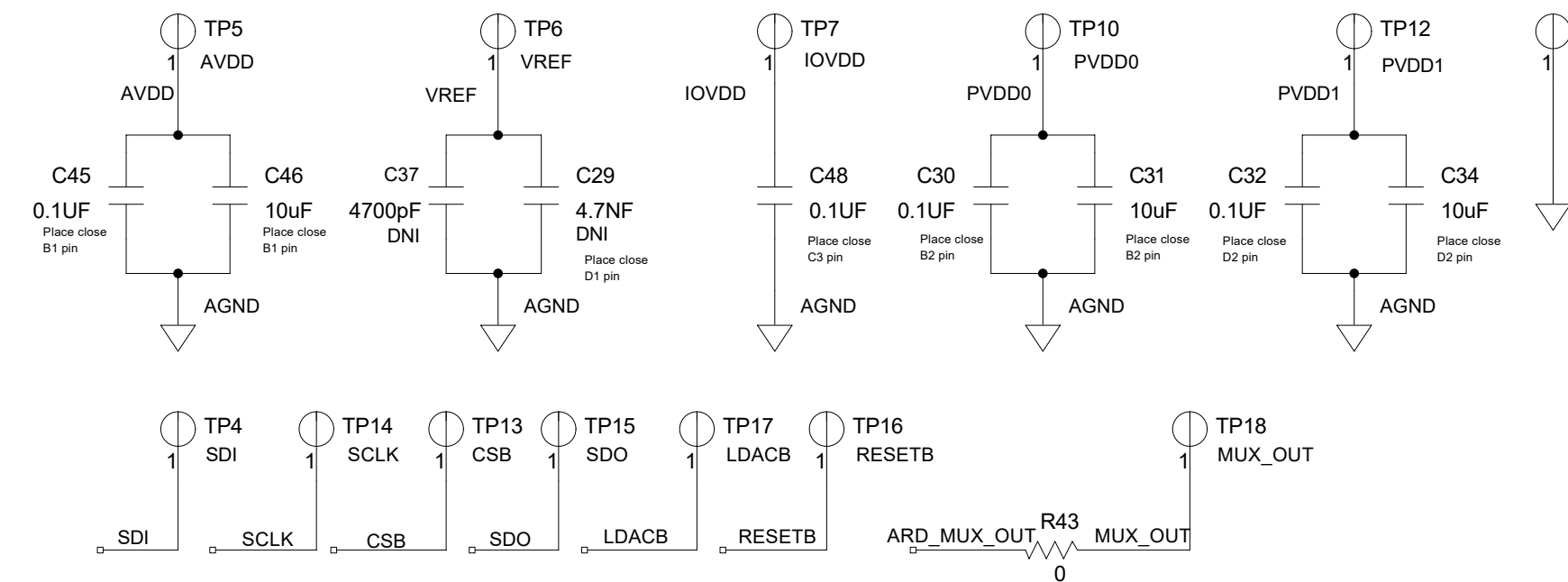
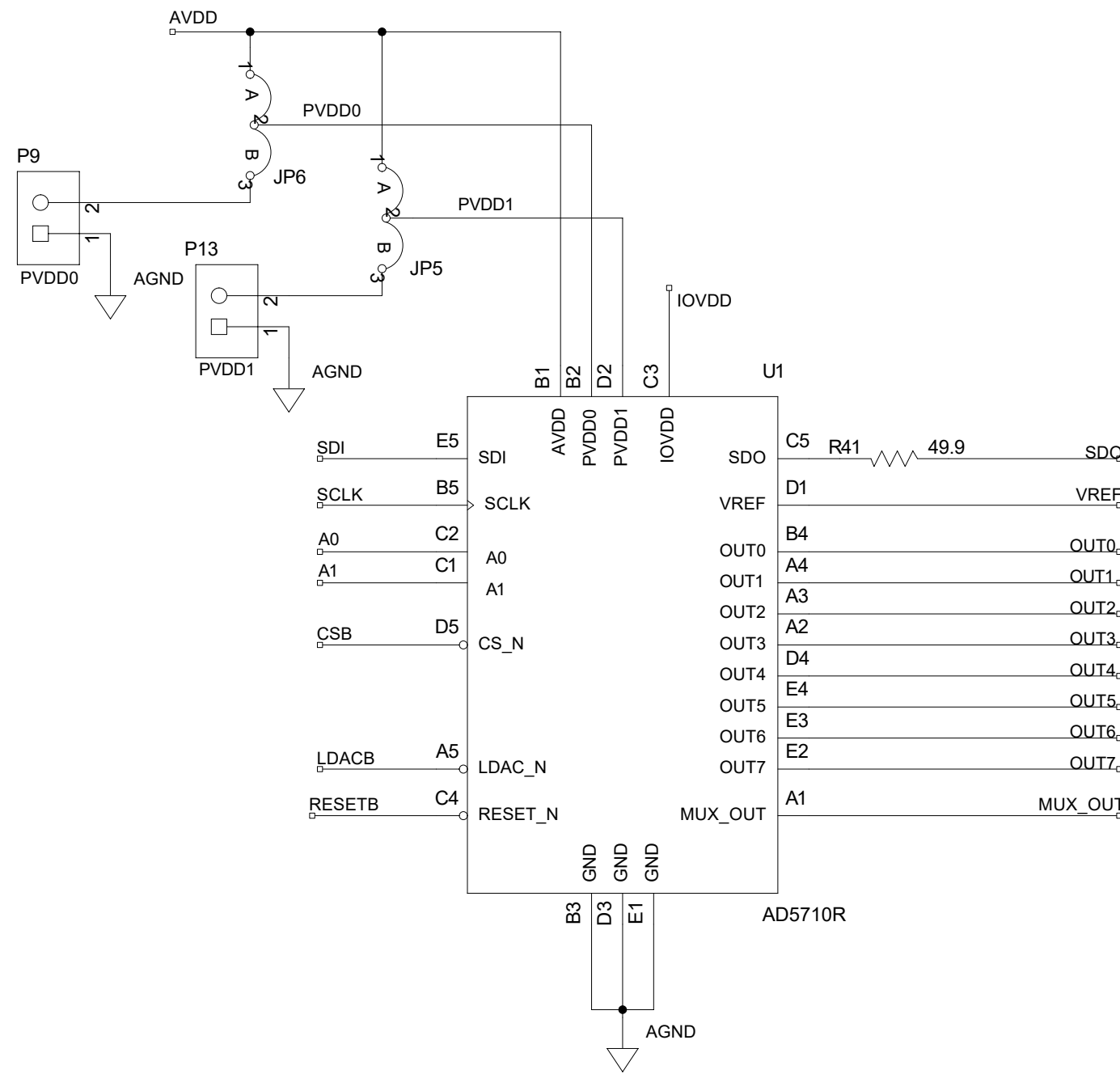
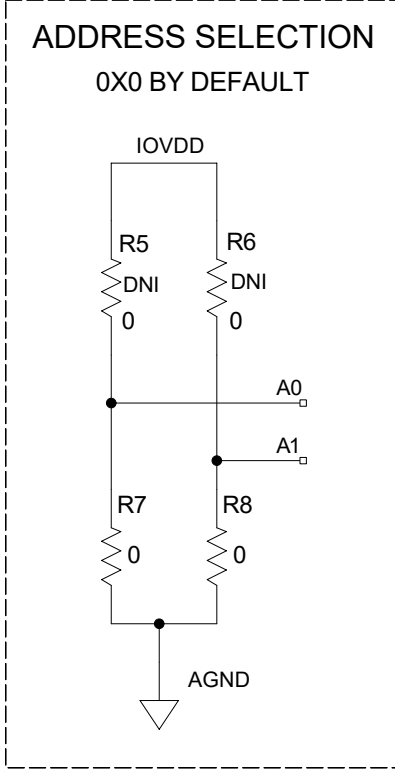


ANALOG DEVICES		SCHEMATIC			
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN ANY MANNER, OR USED FOR ANY OTHER PURPOSES WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.		HW TYPE : Customer Evaluation Z Product(s): AD5710R : AD5711R		DRAWING NO. 02_083459	
		DESIGN VIEW \$DESIGN_VIEW		REV b1	
		PTD ENGINEER V. SETHIA		SIZE D	SCALE 1:1
		SHEET 2 OF 4			

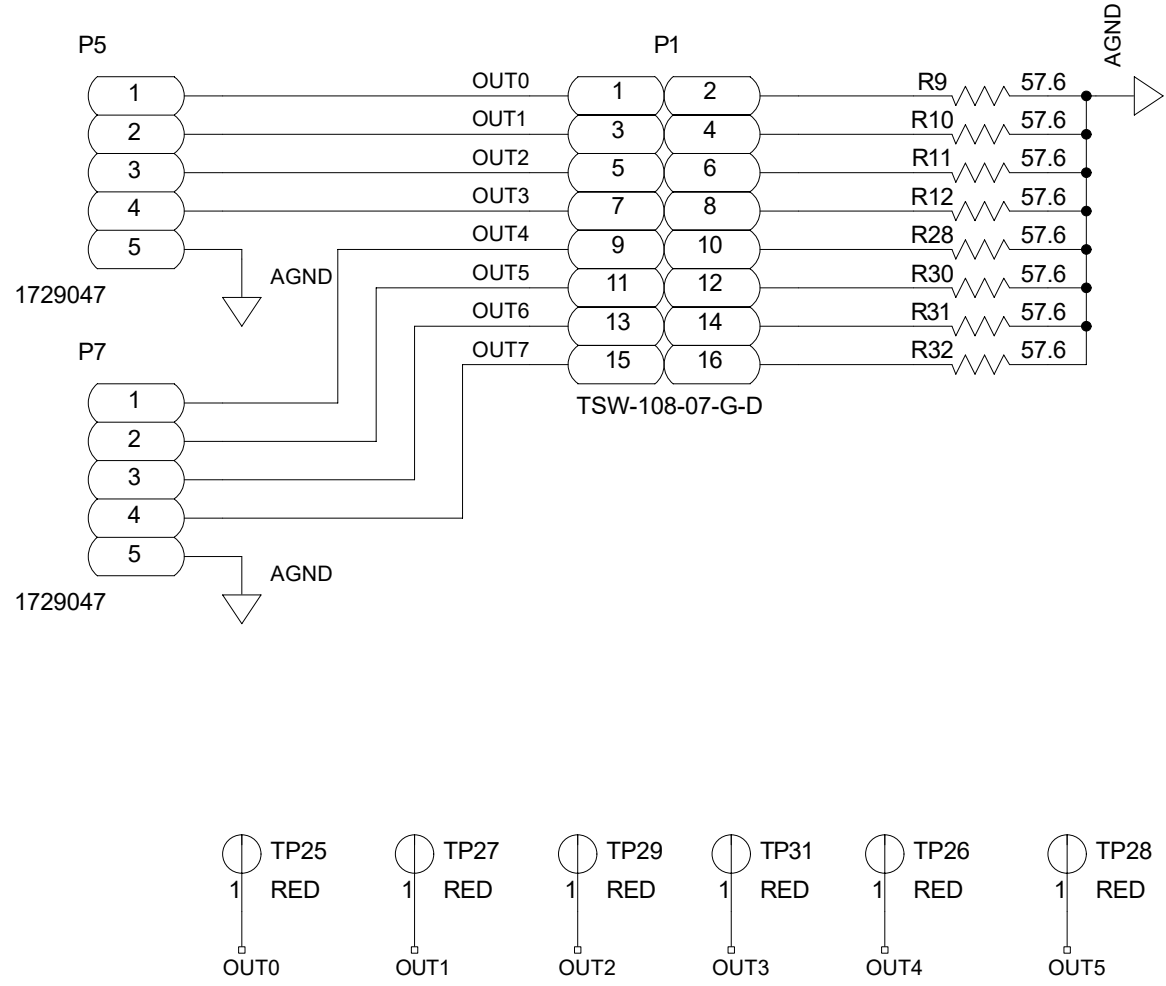
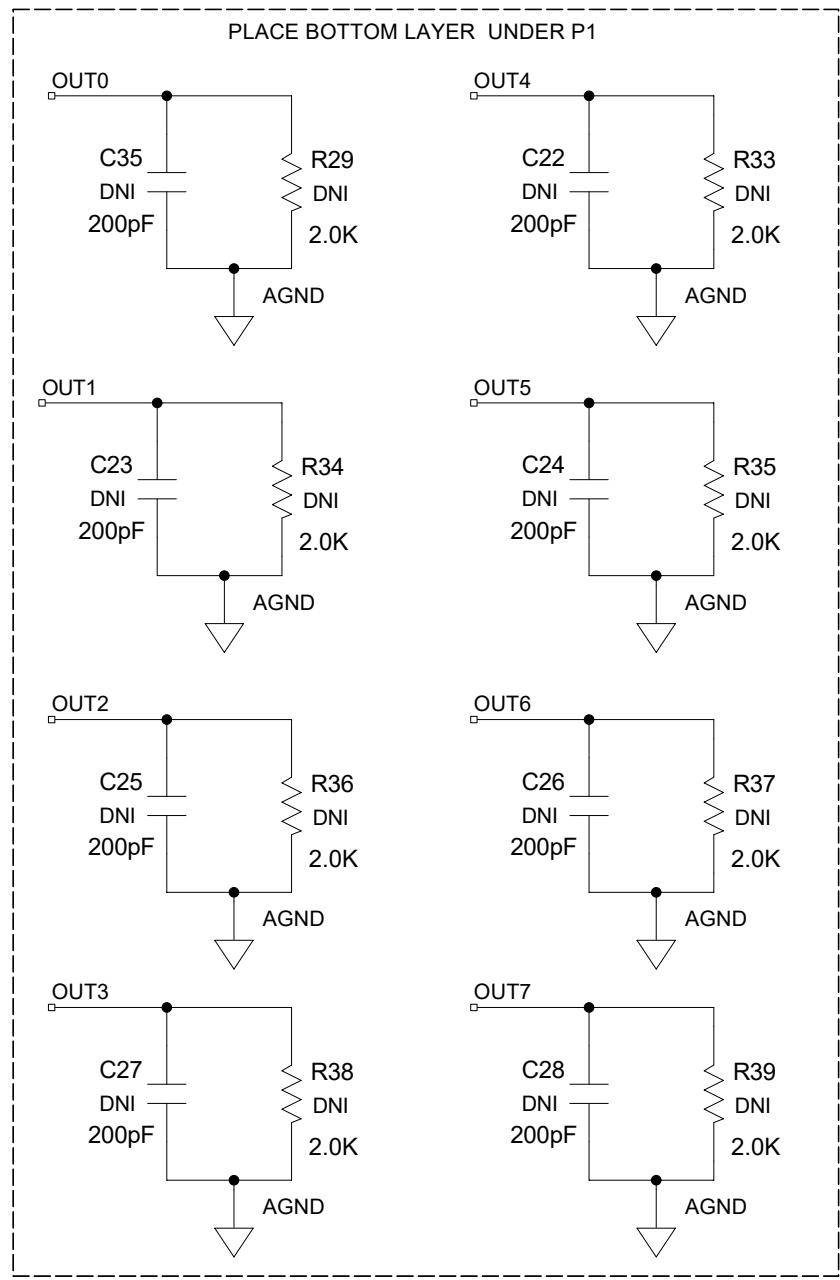
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

MAIN DEVICE CIRCUITRY

MAIN CIRCUIT



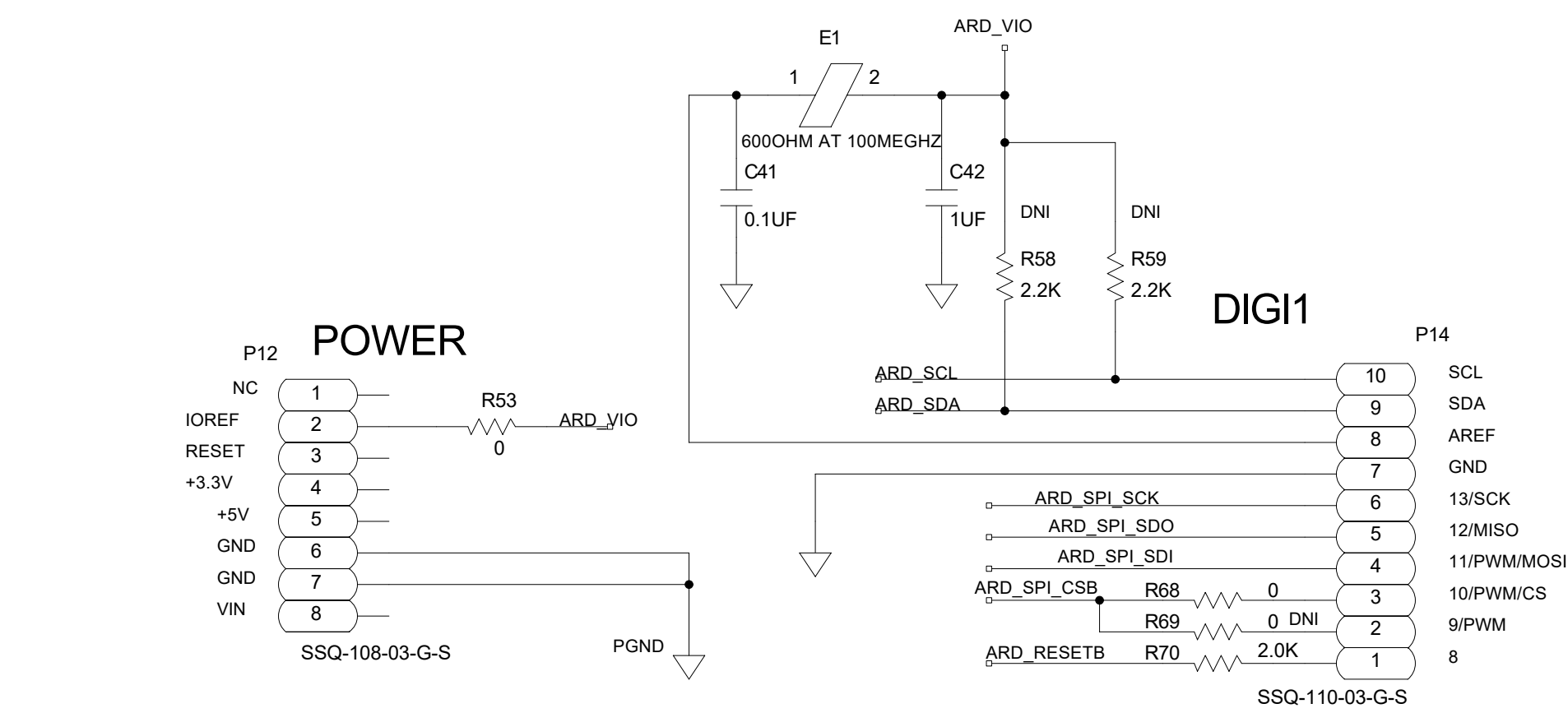
DAC/IDAC OUTPUTS



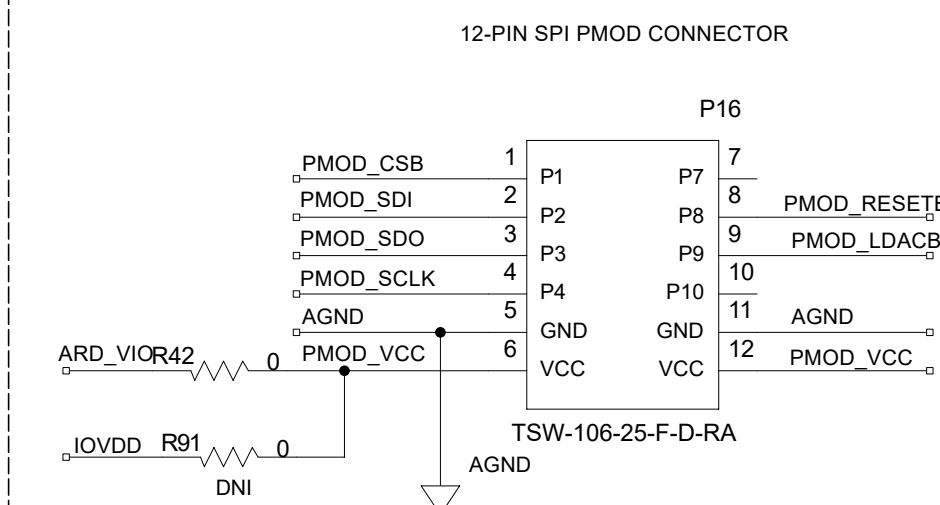
ANALOG DEVICES		SCHEMATIC			
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED IN ANY MANNER, OR USED IN ANY MANNER, WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.		HW TYPE : Customer Evaluation Z Product(s): AD5710R : AD5711R			
		DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_083459		REV b1
		PTD ENGINEER V. SETHIA	SIZE D	SCALE 1:1	SHEET 3 OF 4

DIGITAL CONNECTIVITY

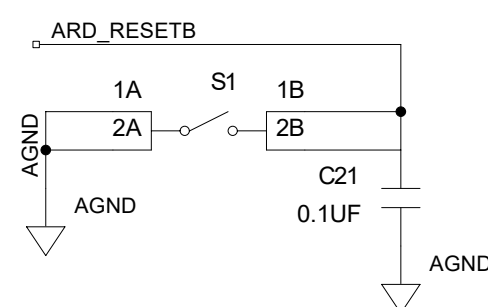
ARDUINO HEADERS



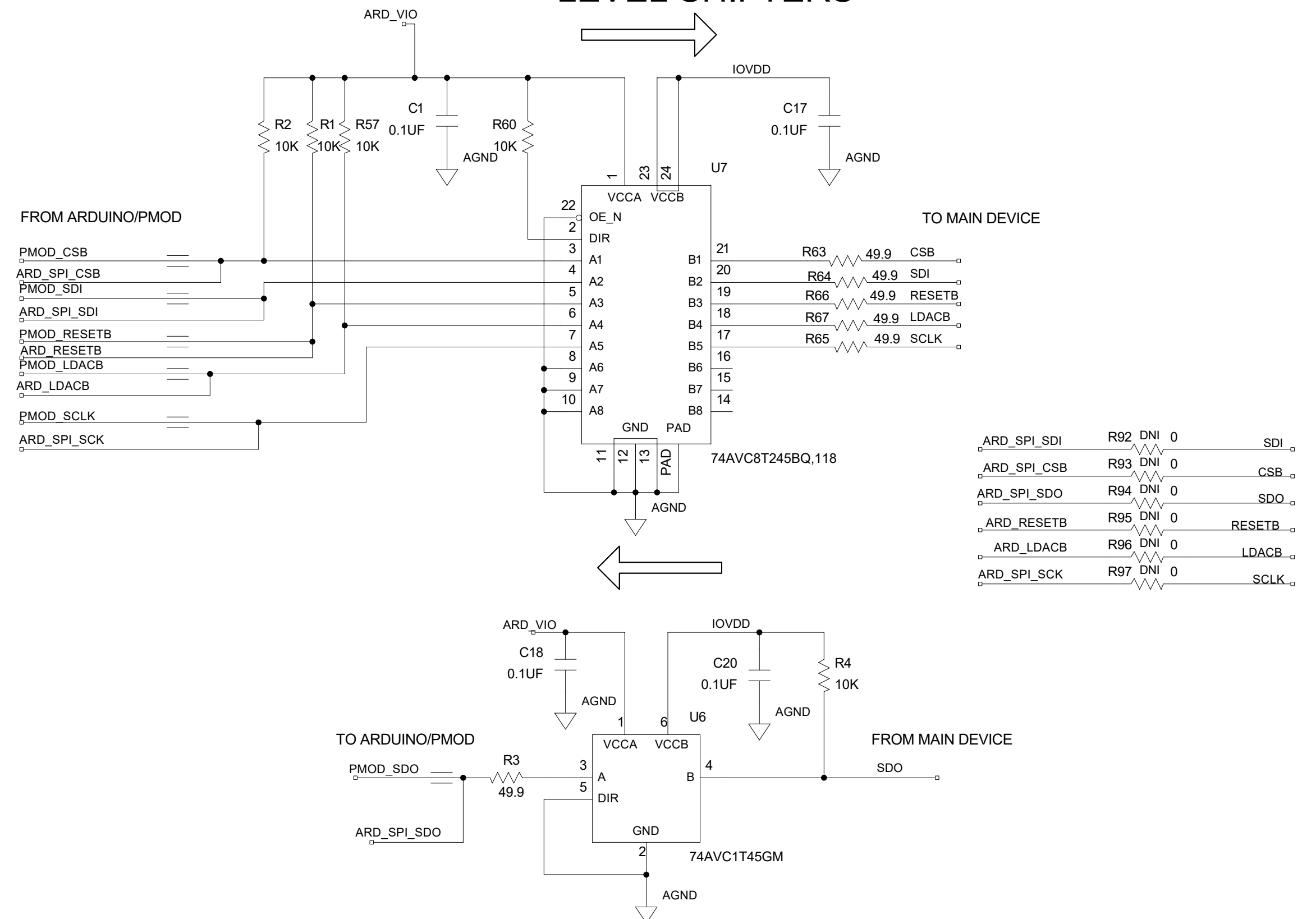
DIGITAL I/F HEADERS



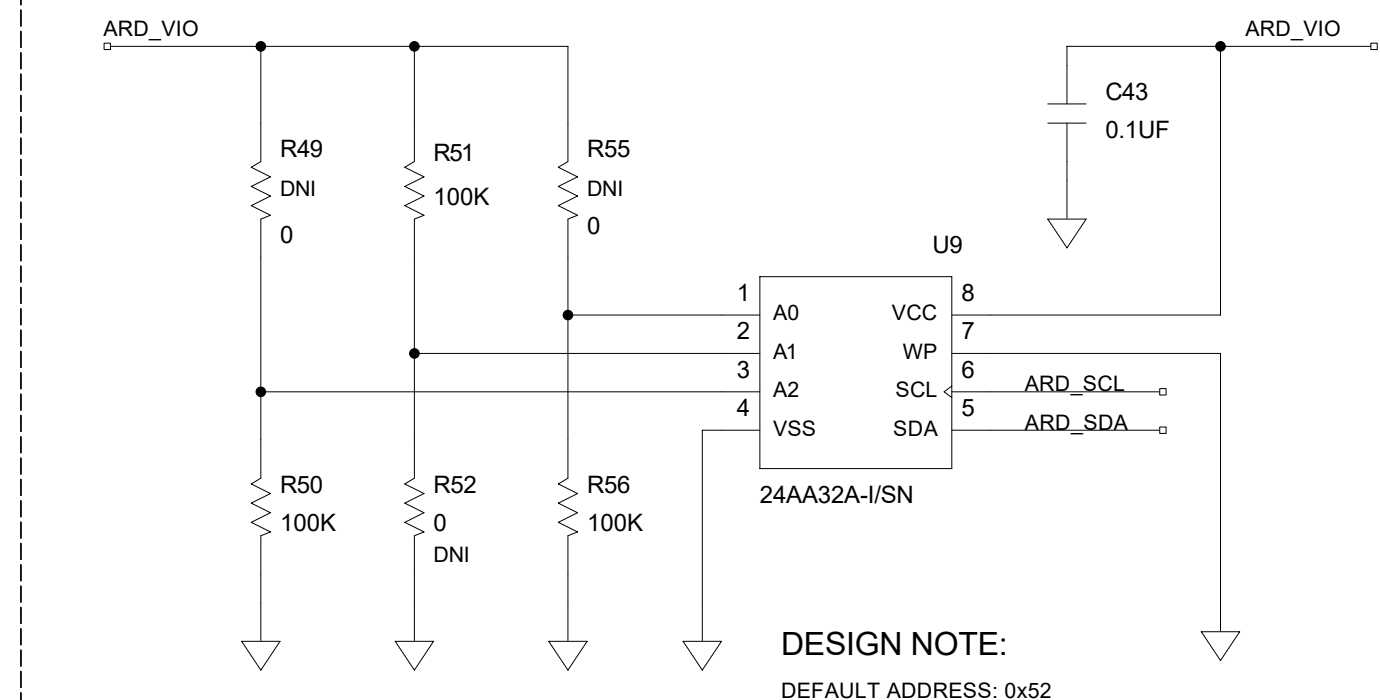
Reset Button



LEVEL SHIFTERS



EEPROM - BOARD ID



THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN ANY MANNER
WITHOUT WRITTEN PERMISSION FROM ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

SCHEMATIC				
HW TYPE : Customer Evaluation Z Product(s): AD5710R : AD5711R				
DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_083459		REV b1	
PTD ENGINEER V. SETHIA	SIZE D	SCALE 1:1	SHEET 4	OF 4